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Patent
706316-1178

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:

Inventor: Takahide Ohkami.

Serial No.: 09/879,658

Filed: June 11, 2001

Group Art Unit: 2123

Examiner: Ayal I Sharon

For: Hardware-Assisted Design Verification
System Using A Packet-Based Protocol
Logic Synthesized For Efficient Data
Loading And Unloading

) Confirmation No. 2956

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I hereby certify that this correspondence is being
deposited with the United States Postal Service as
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on February 18, 2005

Date of Deposit

Jeffrey A. Miller
Name of person Mailing Correspondence

RESPONSE TO OFFICE ACTION

Mail Stop: Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is responsive to the Office Action mailed on November 29, 2004.

Recitation of the pending Claims are reflected in the listing of claims, which begins on page 2 of this paper.

Amendments to the Drawings begin on page 7 of this paper and include an attached replacement sheet.

Remarks/Arguments begin on page 8 of this paper.